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APPLICANTS

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on
 ** CONTINUING DATA *****

** FOREIGN APPLICATIONS *****
 JAPAN 2003-050643 02/27/2003 *on*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>on</i>	STATE OR COUNTRY JAPAN	SHEETS DRAWING 2	TOTAL CLAIMS 5	INDEPENDENT CLAIMS 2
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Verified and Acknowledged
 Examiner's Signature *on* Initials

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TITLE
 Semiconductor integrated circuit device configured to prevent the generation of a reverse current in a MOS transistor

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